

New Product

N-Channel 20-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	r _{DS(on)} (Ω)	I _D (A) ^a	Q _g (Typ)	
20	0.037 at V _{GS} = 4.5 V	6	5.6 nC	
20	0.065 at V _{GS} = 2.5V	6	5.0 110	

FEATURES

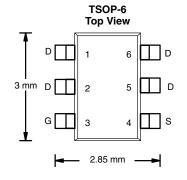
- TrenchFET® Power MOSFET
- New Thermally Enhanced PowerPAK[®]
 ChipFET[®] Package
 Small Footprint Area

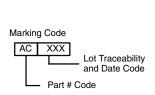
 - Low On-Resistance
 - Thin 0.8-mm Profile

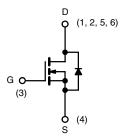


APPLICATIONS

- · Load Switch for Portable Applications
- · Small High Frequency DC-DC converter







Ordering Information: Si3446ADV-T1-E3 (Lead (Pb)-free)

N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T_A	= 25 °C, unless oth	nerwise noted			
Parameter	Symbol	Limit	Unit		
Drain-Source Voltage	V _{DS}	20	V		
Gate-Source Voltage		V _{GS}	± 12	v	
	T _C = 25 °C		6 ^a		
Continuous Drain Current (T _{.I} = 150 °C)	T _C = 70 °C		5.9		
Continuous Drain Current (1, = 150 °C)	T _A = 25 °C	- I _D	5.8 ^{b,c}		
	T _A = 70 °C		4.7 ^{b,c}	Α	
Pulsed Drain Current		I _{DM}	20		
Continuous Courses Dunin Biode Coursest	T _C = 25 °C		2.7		
Continuous Source-Drain Diode Current	T _A = 25 °C	- I _S	1.7 ^{b,c}		
	T _C = 25 °C		3.2		
M	T _C = 70 °C		2.1		
Maximum Power Dissipation	T _A = 25 °C	P _D	2 ^{b,c}	W	
	T _A = 70 °C	1	1.25 ^{b,c}		
Operating Junction and Storage Temperature Range		T _J , T _{stq}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS						
Parameter	Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient ^{b, d}	t ≤ 5 sec	R _{thJA}	51	62.5	°C/W	
Maximum Junction-to-Foot	Steady State	R_{thJF}	32	39]	

Notes:

- a. Package Limited.
- b. Surface mounted on 1" x 1" FR4 board.
- c. t = 5 sec.
 d. Maximum under steady state conditions is 110 °C/W.

Vishay Siliconix

New Product



SPECIFICATIONS T _J = 25 °C, unless otherwise noted							
Parameter	Symbol	Test Condition	Min	Тур	Max	Unit	
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V, } I_D = 250 \mu\text{A}$	20			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	$I_D = 250 \mu\text{A}$		21.5		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	<u> </u>		- 4		IIIV/ C	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	8.0		1.8	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}$			1	μА	
Zeio date voltage Diam Guitem		$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			10		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	20			Α	
Durin Oranga On Olata Davida and	r	$V_{GS} = 4.5 \text{ V}, I_D = 5.8 \text{ A}$		0.031	0.037	Ω	
Drain-Source On-State Resistance ^a	r _{DS(on)}	V _{GS} = 2.5 V, I _D = 1.5 A		0.053	0.065		
Forward Transconductance ^a	9 _{fs}	$V_{DS} = 10 \text{ V}, I_D = 5.8 \text{ A}$		15		S	
Dynamic ^b						•	
Input Capacitance	C _{iss}			640		pF	
Output Capacitance	C _{oss}	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		110			
Reverse Transfer Capacitance	C _{rss}			60			
T. 10 1 01		$V_{DS} = 10 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 5.8 \text{ A}$		13	20	nC	
Total Gate Charge				5.6	9		
Gate-Source Charge	Q_{gs}	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 5.8 \text{ A}$		1.45			
Gate-Drain Charge	Q _{gd}			1.4			
Gate Resistance	R_{g}	f = 1 MHz		2.8		Ω	
Turn-On Delay Time	t _{d(on)}			50	75		
Rise Time	t _r	$V_{DD} = 10 \text{ V}, R_{L} = 2.1 \Omega$		120	180	ns	
Turn-Off DelayTime	t _{d(off)}	$I_D \cong 4.7 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$		30	45		
Fall Time	t _f	Ű		40	60		
Turn-On Delay Time	t _{d(on)}			7	15		
Rise Time	t _r	$V_{DD} = 10 \text{ V}, R_{L} = 2.1 \Omega$		86	130		
Turn-Off DelayTime	t _{d(off)}	$I_D \cong 4.7 \text{ A}, V_{GEN} = 10 \text{ V}, R_q = 1 \Omega$		25	40		
Fall Time	t _f	•		10	15		
Drain-Source Body Diode Characterist				1	1	<u> </u>	
Continous Source-Drain Diode Current	I _S	T _C = 25 °C			6		
Pulse Diode Forward Current	I _{SM}	-			20	A	
Body Diode Voltage	V _{SD}	I _S = 4.7 A, V _{GS} = 0 V		0.8	1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			21	40	ns	
Body Diode Reverse Recovery Charge	Q _{rr}			12	25	nC	
Reverse Recovery Fall Time	t _a	$I_F = 4.7 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^{\circ}\text{C}$		13			
Reverse Recovery Rise Time	t _b			8		ns	

Notes:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

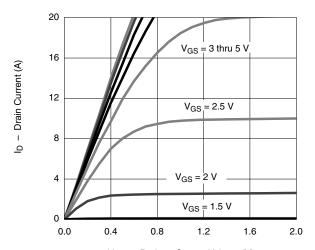
a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.



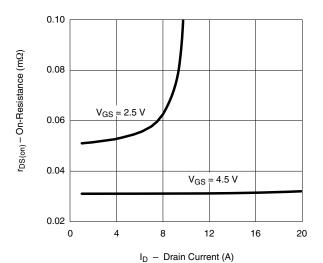
New Product

TYPICAL CHARACTERISTICS 25 °C unless noted

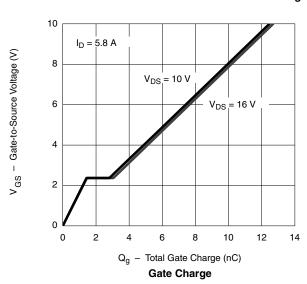


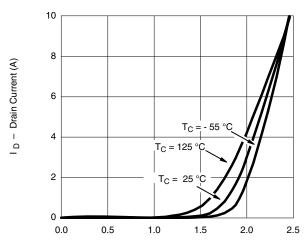
V_{DS} - Drain-to-Source Voltage (V)

Output Characteristics

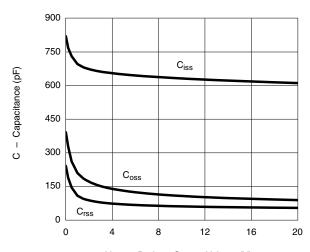


On-Resistance vs. Drain Current and Gate Voltage

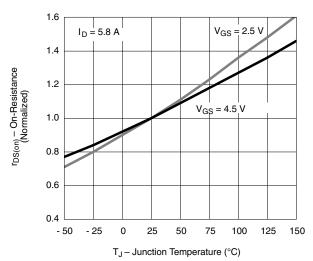




V_{GS} - Gate-to-Source Voltage (V) **Transfer Characteristics**



V_{DS} - Drain-to-Source Voltage (V) **Capacitance**

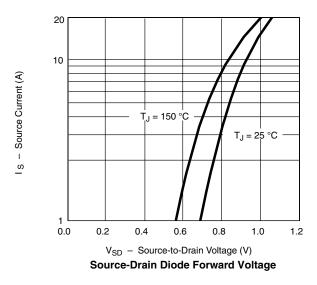


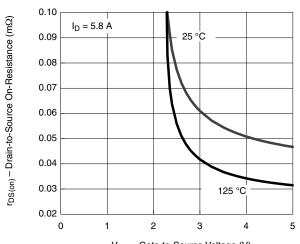
On-Resistance vs. Junction Temperature

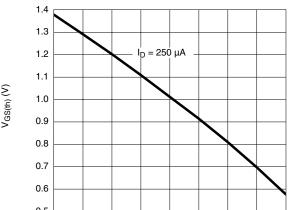
Vishay Siliconix

New Product

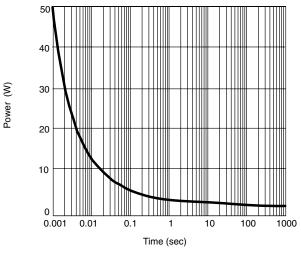
TYPICAL CHARACTERISTICS 25 °C unless noted







V_{GS} - Gate-to-Source Voltage (V) On-Resistance vs. Gate-to-Source Temperature



T_{.I} - Temperature (°C) **Threshold Voltage**

50

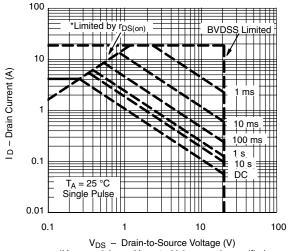
75

100

125

150

Single Pulse Power, Junction-to-Ambient



 $^{\star}V_{GS} > minimum \ V_{GS} \ at \ which \ r_{DS(on)} \ is \ specified$

Safe Operating Area, Junction-to-Ambient

- 50

- 25

0

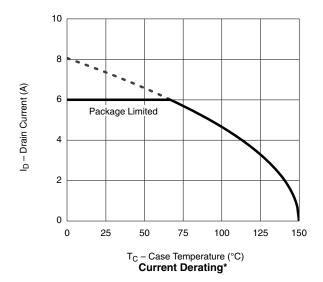
25

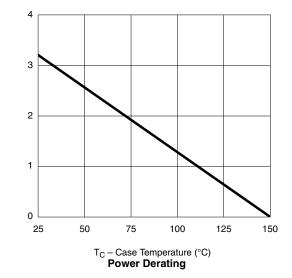




Vishay Semiconductors

TYPICAL CHARACTERISTICS 25 °C unless noted





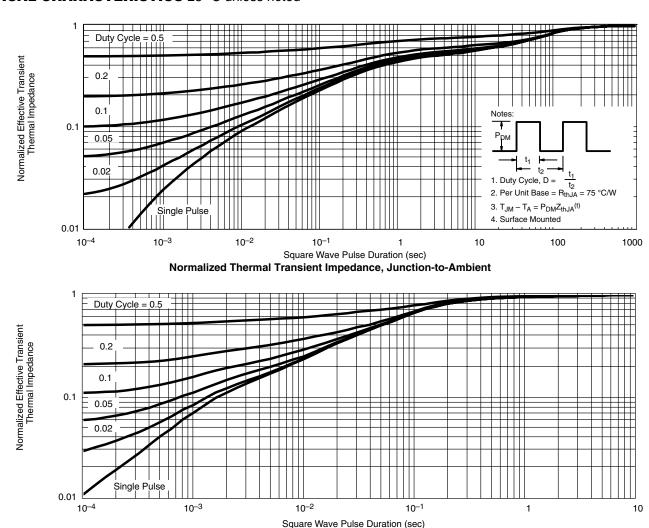
Power Dissipation (W)

^{*}The power dissipation PD is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

Vishay Siliconix

VISHAY

TYPICAL CHARACTERISTICS 25 °C unless noted



Normalized Thermal Transient Impedance, Junction-to-Case

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see http://www.vishay.com/ppg?73772.



Vishay

Disclaimer

All product specifications and data are subject to change without notice.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained herein or in any other disclosure relating to any product.

Vishay disclaims any and all liability arising out of the use or application of any product described herein or of any information provided herein to the maximum extent permitted by law. The product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein, which apply to these products.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay.

The products shown herein are not designed for use in medical, life-saving, or life-sustaining applications unless otherwise expressly indicated. Customers using or selling Vishay products not expressly indicated for use in such applications do so entirely at their own risk and agree to fully indemnify Vishay for any damages arising or resulting from such use or sale. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

Product names and markings noted herein may be trademarks of their respective owners.

Revision: 18-Jul-08

Document Number: 91000 www.vishay.com